

LIST OF ART CITED BY APPLICANT
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Weimin Li et alFILING DATE
January 20, 1999GROUP
2818

U.S. PATENT DOCUMENTS

EXAMINER & TRA Initial	Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
DV	4,775,478	07/1988	Abernathy et al			
	4,870,470	09/1989	Bass et al			
	5,312,768	05/1994	Gonzalez			
	5,439,838	08/1995	Yang			
	6,001,747	12/1999	Annapragada			
	6,518,122	02/2003	Chan			
	6,492,688	12/2002	Ilg			
	6,373,114	04/2002	Jeng			
DV	2001/0019868	09/2001	Gonzalez et al			
AJ						
AK						
AL						

FOREIGN PATENT DOCUMENTS

	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
AM							
AN							
AO							
AP							
AQ							

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)

DV	AR	Wolf, S., "Silicon Processing for the VLSI Era," Vol. 1, pp. 189-190.					
	AS						
	AT						

EXAMINER

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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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